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FIG.1

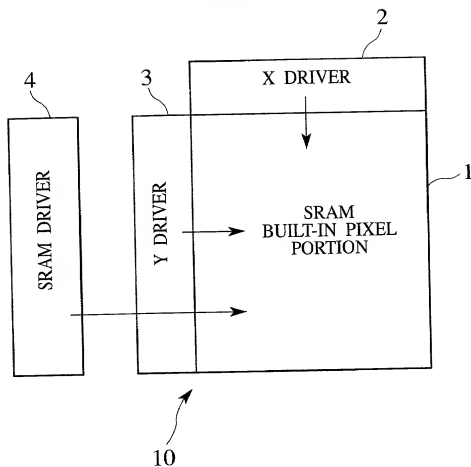


FIG.2

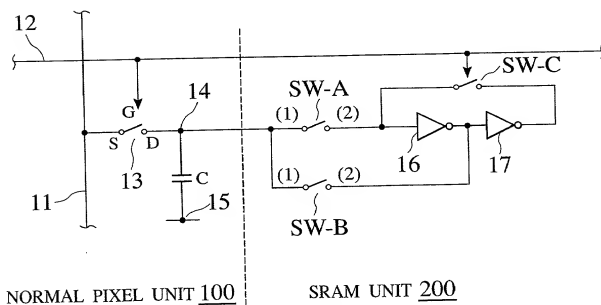
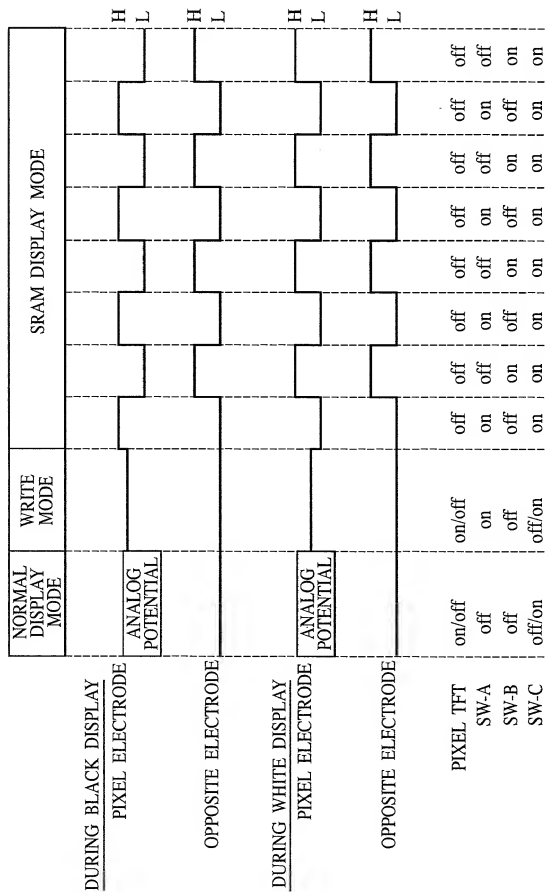


FIG. 3



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FIG.4

CIRCUIT CONFIGURATION	POWER SOURCE VOLTAGE		SRAM DRIVE
	VDD	VSS	
<ul style="list-style-type: none"> • X DRIVER SHIFT REGISTER DATA LATCH GRADATION VOLTAGE SELECTION UNIT SIGNAL LINE OUTPUT UNIT 	XVDD	GND	UNREQUIRED
<ul style="list-style-type: none"> • Y DRIVER SHIFT REGISTER LEVEL SHIFTER SCAN LINE OUTPUT UNIT 	YVDD YGVDD YGVDD	GND YGVSS YGVSS	REQUIRED REQUIRED REQUIRED
<ul style="list-style-type: none"> • SRAM DRIVER SRAM CONTROL SIGNAL GENERATING UNIT SRAM INVERTER POWER SOURCE UNIT 	YGVDD SVDD	YGVSS SVSS	REQUIRED REQUIRED

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FIG.5

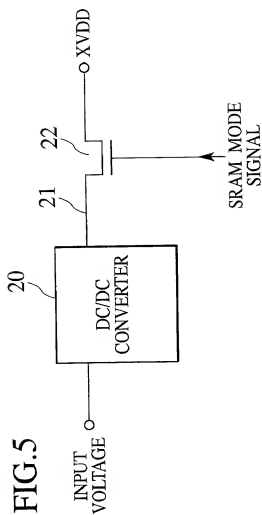


FIG.6

